## Step Up Type DC/DC Converter for White LED

KAB3405T is a high efficient Step-Up Type DC/DC Converter specially designed for constant current drive of White LED. This IC can drive 2-10 white LEDs connected series using a Li-ion Battery. This IC contains N-ch MOS-FET Transistor for Coil-Switching, and LED Current ( $\mathrm{I}_{\mathrm{F}}$ ) is set with an external resistor.

## Features

- Variable LED Current $\mathrm{I}_{\mathrm{F}}$ is set with a external resistor
: 20 mA (Typ.) @ $\mathrm{R}_{\text {SENS }}=15 \Omega$
- High Efficiency
- Output Over Voltage Shutdown
- Built in output over voltage protect circuit.
- Switching Frequency : 1.0 MHz (Typ.)


## Applications

- Back light white LEDs in LCD of PDA, Cellular Phone
- Handy Terminal Equipment.



## Basic Application Circuit



## Block Diagram



Pin Function

| Pin No. | Symbol | Function Description |
| :---: | :---: | :--- |
| 1 | SHDN | Voltage-Input Terminal for IC-Enable / Disable LED-I $\mathrm{I}_{\mathrm{F}}$. $\mathrm{I}_{\mathrm{F}}$ adjustment with PWM input signal is also available. |
| 2 | OVD | Over Voltage Detection Terminal. IC Switching Operation is disabled with detection over voltage. If the voltage returns <br> to detection level or less, Operation is enabled again. |
| 3 | $\mathrm{~V}_{\mathrm{IN}}$ | Supply Voltage Input Terminal. (2.8V to 5.5V) |
| 4 | SW | Switch Terminal for DC/DC Converter. Nch MOSFET Built-In. |
| 5 | GND | Ground Terminal. |
| 6 | FB | ${\text { LED I } \mathrm{I}_{\mathrm{F}} \text { Setting Resister Connecting Terminal. }}$ |

## AUTE MAXIMUM RATINGS ( $\mathbf{T a}=\mathbf{2 5}^{\boldsymbol{\circ}} \mathbf{C}$ if without notice)

| Characteristics | Symbol | Ratings | Unit |
| :---: | :---: | :---: | :---: |
| Power Supply Voltage | $\mathrm{V}_{\text {IN }}$ | -0.3 to +6.0 | V |
| Input Voltage | $\mathrm{V}_{\text {IN(SHDN }}$ | -0.3 to $+\mathrm{V}_{\text {IN }}+0.3$ | V |
| Switching Terminal Voltage | $\mathrm{V}_{\mathrm{O} \text { (SW) }}$ | -0.3 to 40 | V |
| Switching Terminal Current | $\mathrm{I}_{\text {( }} \mathrm{SW}$ ) | 1500 | mA |
| Power Dissipation | $\mathrm{P}_{\mathrm{D}}$ | 0.41 (Device) | W |
|  |  | 0.47 (on PCB)Caution 2 |  |
| Thermal Resistance | $\mathrm{R}_{\mathrm{th}(\mathrm{j}-\mathrm{a})}$ | 300(Device) | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  |  | 260(on PCB) |  |
| Operation Temperature Range | $\mathrm{T}_{\text {opr }}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Maximum Junction Temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |

Caution 2: Power Dissipation must be calculated with subtraction of $3.8 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ from Maximum Rating with every $1{ }^{\circ} \mathrm{C}$ if Topr is upper $25^{\circ} \mathrm{C}$. (on PCB)

ELECTRICAL CHARACTERISTICS ( $\mathbf{T a}=25^{\circ} \mathrm{C}, \mathrm{V}_{\text {IN }}=2.8 \mathrm{~V}$ to 5.5 V if without notice)

| Characteristics | Symbol | Test Condition | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage Range | $\mathrm{V}_{\text {IN }}$ | - | 2.8 | - | 5.5 | V |
| Operating Consumption Current | $\mathrm{I}_{\mathrm{N}(\mathrm{ON})}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{R}_{\text {SENS }}=15 \Omega$ | - | 0.6 | 0.9 | mA |
| Quiescent Consumption Current | $\mathrm{I}_{\mathrm{N} \text { (Off) }}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=0 \mathrm{~V}$ | - | 0.5 | 1.0 | $\mu \mathrm{A}$ |
| SHDNTerminal 'H' Level Input Voltage | $\mathrm{V}_{\text {SHDNH }}$ | - | 1.3 | - | $\mathrm{V}_{\text {IN }}$ | V |
| SHDN Terminal 'L' Level Input Voltage | $\mathrm{V}_{\text {SHDNL }}$ | - | 0 | - | 0.4 | V |
| SHDN Terminal Current | $\mathrm{I}_{\text {SHDN }}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=3.6 \mathrm{~V}$ or 0 V | - | 0 | 1.0 | $\mu \mathrm{A}$ |
| Integrated MOS-Tr Switching requency | $\mathrm{f}_{\text {OSC }}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=3.6 \mathrm{~V}$ | 0.77 | 1.0 | 1.43 | MHz |
| Switching Terminal Leak Current | $\mathrm{I}_{\text {OZ(SW }}$ | - | - | 0.5 | 1 | $\mu \mathrm{A}$ |
| FB Terminal Feedback Voltage ( $\mathrm{V}_{\mathrm{FB}}$ ) | $\mathrm{V}_{\text {FB }}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{R}_{\text {SENS }}=15 \Omega, \mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{L}=22 \mu \mathrm{H}$ | 285 | 300 | 315 | mV |
|  |  | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{R}_{\text {SENS }}=15 \Omega, \mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{L}=22 \mu \mathrm{H}$ | 285 | 300 | 315 | mV |
| FB Terminal Line Regulation | $\mathrm{V}_{\mathrm{FB}}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}$ center $\mathrm{V}_{\text {IN }}=3.0$ to 5.0 V | -5 | - | 5 | \% |
| FB Terminal Current | $\mathrm{I}_{\mathrm{FB}}$ | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {SHDN }}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{FB}}=300 \mathrm{mV}$ | - | 0.02 | - | $\mu \mathrm{A}$ |
| OVD terminal Voltage | $\mathrm{V}_{\text {OVD }}$ | - | 36 | 38 | 39.5 | V |
| OVD Terminal Leakage Current | $\mathrm{I}_{\text {OVD }}$ | $\mathrm{V}_{\text {OVD }}=30 \mathrm{~V}$ | - | 0.5 | 1 | $\mu \mathrm{A}$ |

## Protection in LED opened condition

The operation with OVD terminal is available for the protection in case LED Circuit opened.
When the voltage of OVD terminal is over 38 V (typ.), Nch MOS switching operation is disabled in the IC. When the voltage of OVD terminal drops below 38V (typ.), Nch MOS switching operation becomes available again.
If load of LED is detached, Nch MOS switching operation is disabled with detection of boost circuit voltage and the IC is protected from unexpected over voltage.

## Setting of external Capacitor

In case not using PWM signal to SHDN terminal for brightness control, recommended values are

$$
\mathrm{C}_{\mathrm{IN}}=\text { Over } 2.2(\mu \mathrm{~F}), \mathrm{C}_{\text {OUT }}=\text { Over } 1.0(\mu \mathrm{~F})
$$

In case with PWM signal to SHDN terminal for brightness control, recommended values are

$$
\mathrm{C}_{\mathrm{IN}}=\text { Over } 4.7(\mu \mathrm{~F}), \mathrm{C}_{\text {OUT }}=\text { Under } 1.0(\mu \mathrm{~F})
$$

The recommended values of the capacitors depend on the control of brightness. For details, please see the item "Control of brightness" from the next page.)
These recommended values reduce fluctuation of input current to up accuracy of brightness.
$<$ Please see after page-6>
The capacitor value must be considered for gain enough accuracy of brightness with reduction of noise from Input current changing.

## Setting of External Inductor Size

Please select the inductor size with referring this table corresponding to each number of LEDs.
Recommendation for the case that LED current $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$

| LEDs | Indictor Size | Note |
| :---: | :---: | :---: |
| $2-5$ | $10 \mu \mathrm{H}$ | LED current $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$ |
| Over 6 | $22 \mu \mathrm{H}$ |  |

## Setting of $\mathbf{I}_{\mathbf{F}}$

Resistance connects between $\mathrm{R}_{\text {SENS }}$ pin and GND.
The average current is set by this $\mathrm{R}_{\text {SENS }}$ value and average current are obtained by the following equation.

$$
\mathrm{I}_{\mathrm{F}}[\mathrm{~mA}]=\frac{300[\mathrm{mV}]}{\mathrm{R}_{\mathrm{SENS}}[\Omega]}
$$

Current Value error is within $\pm 5 \%$.

## Current Dimming Control

Recommended Brightness Control Circuits are 4 types.

1) Input PWM signal to SHDN terminal
$\mathrm{I}_{\mathrm{F}}$ can be adjusted with PWM signal by inputting it to SHDN terminal.

## [Notice]

$\ll$ PWM signal frequency>>

- The recommended PWM signal frequency is from 100 Hz to 10 kHz . There is a possibility to arise the audible frequency in mounting to the board because it is within the auditory area.
<<Constant number of external condenser>>
- To reduce the fluctuation of input current and increase the accuracy of brightness, the values that $\mathrm{C}_{\mathrm{IN}}=$ $4.7(\mu \mathrm{~F})$ or more, $\mathrm{C}_{\text {OUT }}=1.0(\mu \mathrm{~F})$ or less are recommended.
- When the PWM signal is off, the time to drain $\mathrm{C}_{\text {OUT }}$ of charge depends on the constant number. And so, the actual value is little different from the theoretical value.
$\ll$ PWM input signal>>
- Set the amplitude of PWM signal within the range of SHDN terminal specification.
$\ll$ Rush current in inputting $\gg$
- In case dimming by inputting the PWM signal to the SHDN terminal, this IC turns on and off repeatedly.
And the rush current, which provides the charge to $\mathrm{C}_{\text {OUT }}$, arises in turning on. Take care in selecting the condenser.
<<Current value in Control with PWM: Ideal Equation>>

$$
\mathrm{I}_{\mathrm{F}}[\mathrm{~mA}]=\frac{300[\mathrm{mV}] \times \text { ON Duty }[\%]}{\mathrm{R}_{\mathrm{SENS}}[\Omega]}
$$

$\ll$ Recommended application>>

2) Input analog voltage to FB terminal
$\mathrm{I}_{\mathrm{F}}$ can be adjusted with Analog voltage input to FB terminal.
This method is without repeating IC ON/OFF, and no consider holding rash current.
[Notice]

- LED current value goes ever $100 \%$ of current set with $\mathrm{R}_{\text {SENS }}$, if the input voltage is between 0 V to 300 mV (typ)
$\ll$ Recommended application>>


3) Input PWM signal with filtering to $F B$ terminal
$\mathrm{I}_{\mathrm{F}}$ can be adjusted with filtering PWM signal using RC filter indicated in recommended circuit, because the PWM signal can be regard as analog voltage after filtering.
This method is without repeating IC ON/OFF, and no need to consider holding rash current.
[Notice]

- LED current value goes over $100 \%$ of the current set with $\mathrm{R}_{\text {SENS }}$, if the input voltage after filtering is between 0 V to 300 mV (typ).
$\ll$ Recommended application>>


4) Input Logic signal
$\mathrm{I}_{\mathrm{F}}$ can be adjusted with Logic signal input as indicated in recommended circuit.
The resistor connected the ON-state Nch MOS Drain and $\mathrm{R}_{\text {SENS }}$ determines $\mathrm{I}_{\mathrm{F}}$.

Average of setting current Io (mA) is next, approximately.
$\mathrm{I}_{\mathrm{F}}=(300[\mathrm{mV}] /$ Sum of resistor value $[\Omega])$

$$
\mathrm{I}_{\mathrm{F}}[\mathrm{~mA}]=\frac{300[\mathrm{mV}]}{\text { Sum of resistor v alue }[\Omega]}
$$

$\ll$ Recommended application>>


| M1 | M2 | LED Current |
| :---: | :---: | :---: |
| OFF | OFF | $\frac{300[\mathrm{mV}]}{\mathrm{R}_{\text {SENS }}[\Omega]}$ |
| ON | OFF | $300[\mathrm{mV}] \times \frac{\mathrm{R}_{\text {SENS }}[\Omega] \mathrm{R} 1[\Omega]}{\mathrm{R}_{\text {SENS }}[\Omega]+\mathrm{R} 1[\Omega]}$ |
| OFF | ON | $300[\mathrm{mV}] \times \frac{\mathrm{R}_{\text {SENS }}[\Omega] \mathrm{R} 2[\Omega]}{\mathrm{R}_{\text {SENS }}[\Omega]+\mathrm{R} 2[\Omega]}$ |
| ON | ON | $300[\mathrm{mV}] \times \frac{\mathrm{R}_{\text {SENS }}[\Omega] \mathrm{R} 1[\Omega] \mathrm{R} 2[\Omega]}{\mathrm{R}_{\text {SENS }}[\Omega] \mathrm{R} 1[\Omega]+\mathrm{R}_{\text {SENS }}[\Omega] \mathrm{R} 2[\Omega]+\mathrm{R} 1[\Omega] \mathrm{R} 2[\Omega]}$ |

